# Prefetch Side-Channel Attacks: Bypassing SMAP and Kernel ASLR

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## Overview

- prefetch instructions don't check privileges
- prefetch instructions leak timing information

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exploit this to:

- locate a driver in kernel = defeat KASLR
- translate virtual to physical addresses

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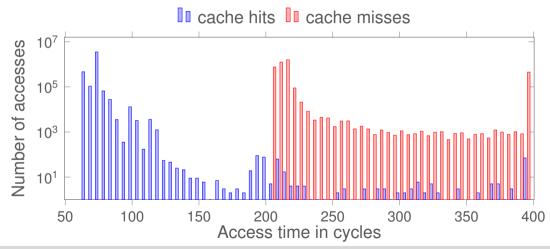
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## **CPU** Caches

Memory (DRAM) is slow compared to the CPU

- buffer frequently used memory
- every memory reference goes through the cache
- based on physical addresses

#### Memory Access Latency



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# Unprivileged cache maintainance

Optimize cache usage:

- prefetch: suggest CPU to load data into cache
- clflush: throw out data from all caches

... based on virtual addresses

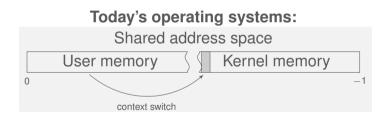
# Software prefetching

prefetch instructions are somewhat unusual

- hints can be ignored by the CPU
- do not check privileges or cause exceptions

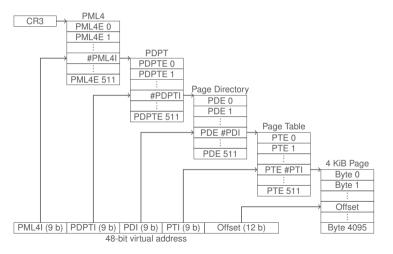
but they do need to translate virtual to physical

# Kernel must be mapped in every address space



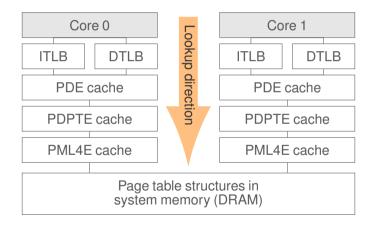
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# Address translation on x86-64

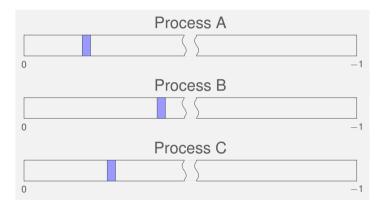


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# Address Translation Caches



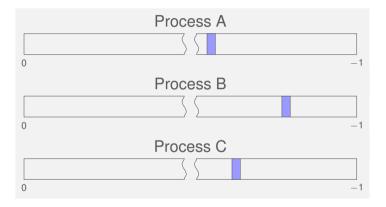
# Address Space Layout Randomization (ASLR)



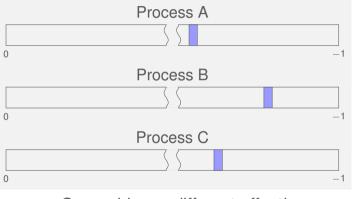
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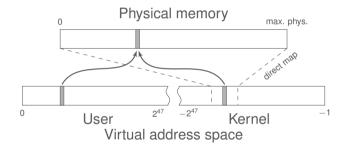


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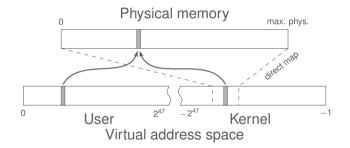


Same driver - different offset!

# Kernel direct-physical map



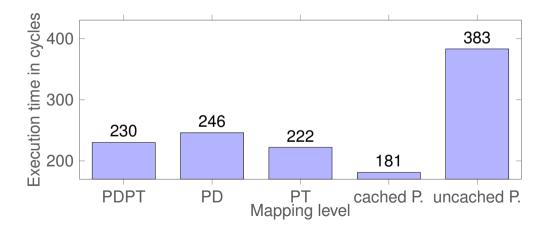
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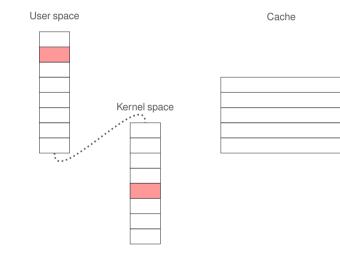


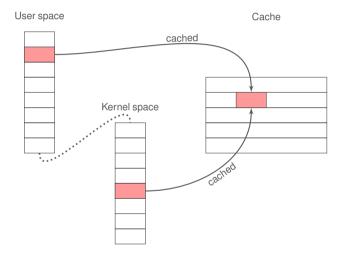
#### OS X, Linux, BSD, Xen PVM (Amazon EC2)

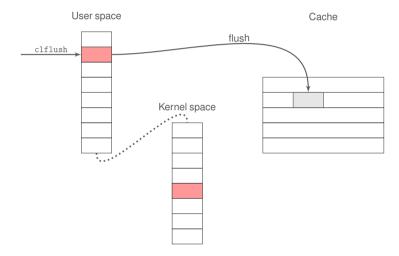
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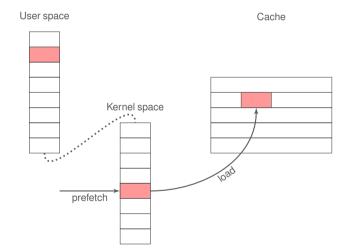
#### Translation-Level Oracle



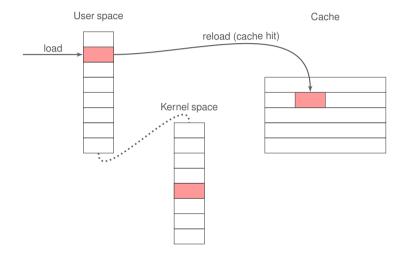




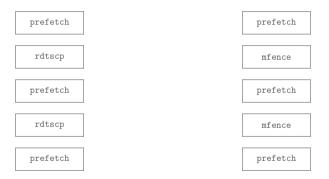




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The CPU may reorder prefetch instruction – a look at rdtscp



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The CPU may reorder instructions



but not over cpuid!

# Windows 10 Memory layout

- HAL, kernel, kernel drivers located bewetween
  - start: 0xffff 8000 0000 0000
  - end:Oxffff 9fff ffff ffff

for all mapped pages (found via translation-level oracle):

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 $\rightarrow$  Fastest average access time is a driver page.

## Breaking Windows KASLR

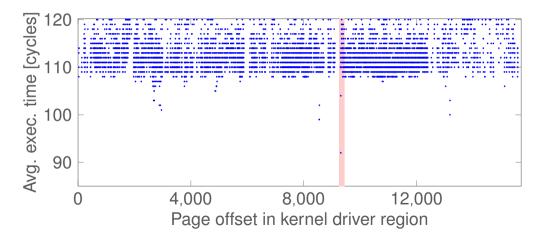
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#### Full attack on Windows 10 in < 12 seconds

#### Locate Kernel Driver (defeat KASLR)



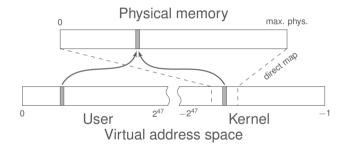
## Kernel exploits (10 years ago)

- overwrite return address
- $\rightarrow$  jump to userspace code
  - overwrite stack pointer
- $\rightarrow$  switch to userspace stack

#### Mitigating kernel exploits

- Jump to userspace code? Nope! Hardware prevents that.
- = supervisor-mode execution prevention (SMEP)
- Switch to userspace stack? Nope! Hardware prevents that.
- = supervisor-mode access prevention (SMAP)

#### Kernel direct-physical map



#### Evading the mitigation

• get direct-physical-map address of userspace address  $\rightarrow$  jump/switch there

known as "ret2dir" attacks (Kemerlis et al. 2014)

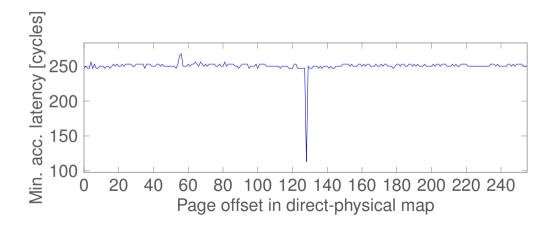
#### Mitigating the evasion

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- getting rid of direct-physical map? Apparently not.
- $\rightarrow\,$  do not leak physical addresses to user

#### Prefetching via direct-physical map

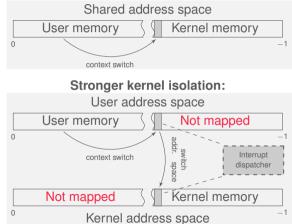


#### Prefetching via direct-physical map

- immediately leaks a direct-physical map address
  - $\rightarrow$  no physical address necessary (compared to ret2dir)
- if direct-physical map offset is known
  - ightarrow leaks physical address

#### Countermeasure

#### Today's operating systems:



#### Conclusion

- prefetch leaks significant information
- we can locate a driver in the kernel and thus break KASLR
- break SMAP/SMEP and get physical addresses
- countermeasure could be implemented in software

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